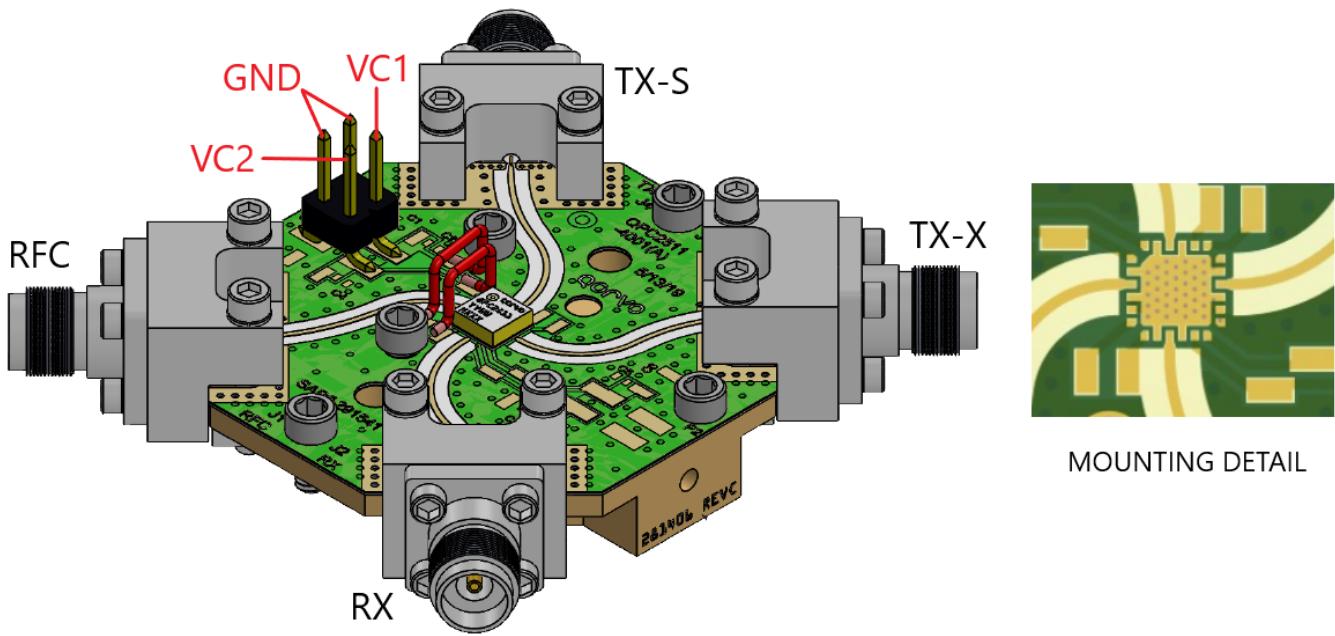
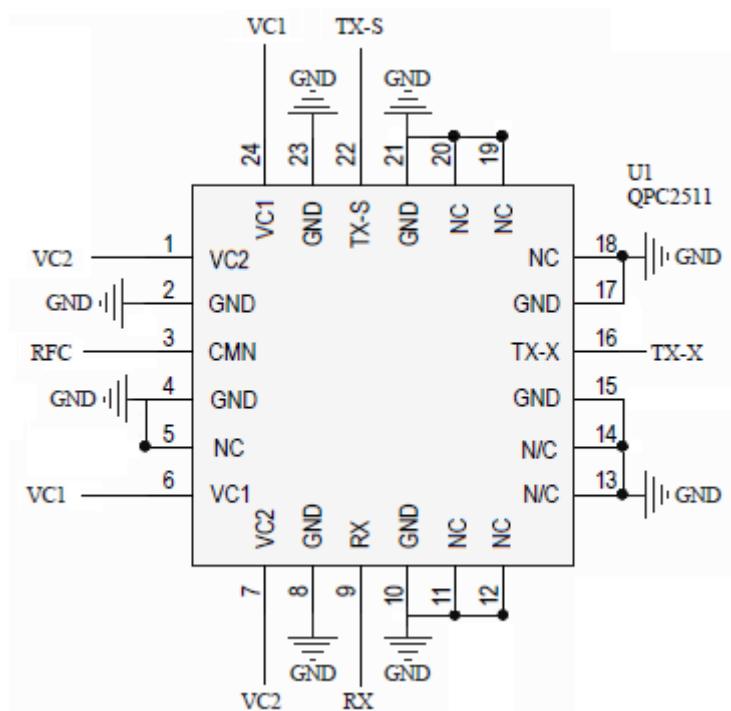


Evaluation Board (EVB) Assembly Layout.

Notes:

1. This switch can be configured as a Single Pole, Single Throw (SPST) by terminating one unused RF switched port with a 50 Ohm load.
2. See Logic Table on page 2 for biasing the voltage controls.
3. External components are not required

Application Circuit



Notes:

1. This switch can be configured as a Single Pole, Single Throw (SPST) by terminating two unused RF switched ports with a 50 Ohm load.
2. External components are not required.

Bias Up Procedure

1. V_{C1} or V_{C2} set to 0 V (see Logic Table for RF Path)
2. V_{C1} or V_{C2} set to -30 V (see Logic Table for RF Path)
3. Apply RF signal to RF Input

Bias Up Down

1. Turn off RF supply
2. Turn V_{C1} or V_{C2} to 0 V
3. Turn V_{C1} or V_{C2} to 0 V

Logic Table (SP3T Truth Table)

RF Path	Operating Mode	State	V_{C1}	V_{C2}
RFC to RF1 ON	10 W RX	On-State (Insertion Loss), TX-S & TX-X = OFF	1	0
RFC to RF2/RF3 ON	30 W TX, S-Band and X-Band	On-State (Insertion Loss), RX = OFF	0	1

- VC High (1) = 0 V
- VC Low (0) = -22, -24, -26, -28 or -30 V