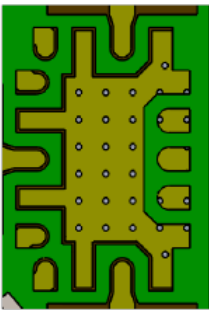
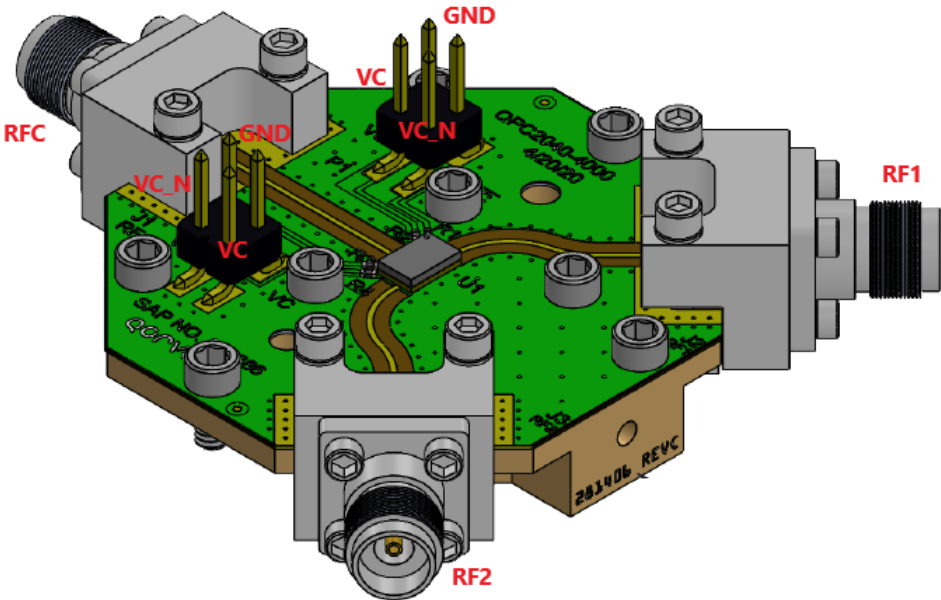




Evaluation Board (EVB) Assembly Layout.



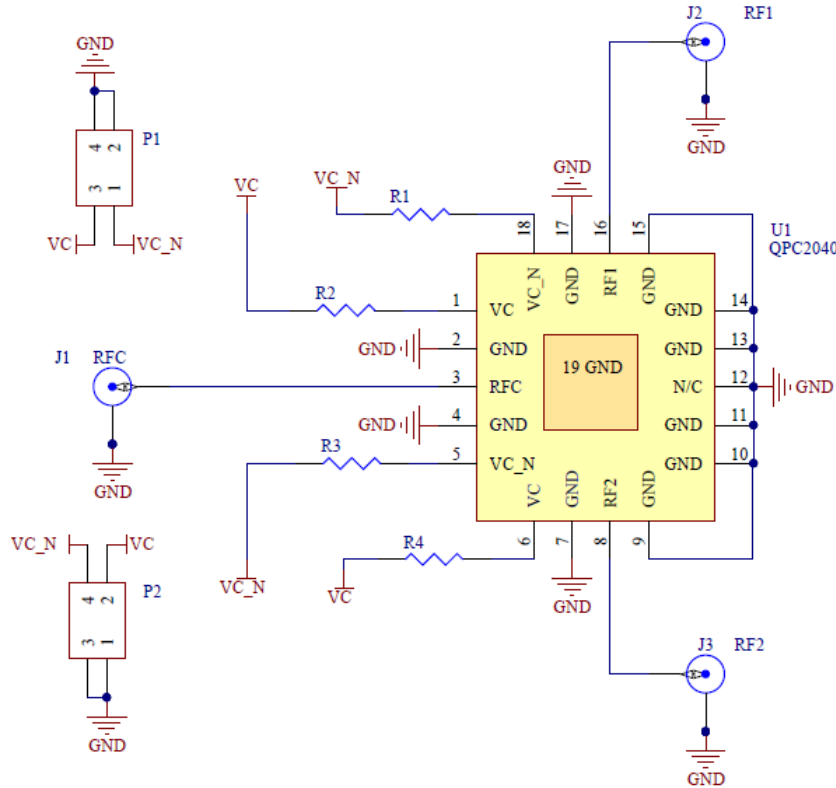
MOUNTING DETAIL

- Notes:
- 1. This switch can be configured as a Single Pole, Single Throw (SPST) by terminating one unused RF switched port with a 50 Ohm load.
  - 2.  $V_C$  can be biased from either top or bottom pin and the non-biased pin can be left open.
  - 3.  $V_{C\_N}$  can be biased from either top or bottom pin and the non-biased pin can be left open.
  - 4. External components are required

Bill of Materials for EVB – QPC2040

Reference Des.	Value	Description	Manuf.	Part Number
R1, R2, R3, R4	1.1 K $\Omega$	RES, 0402, 1%, 1/10 W	Various	–

### Application Circuit



#### Notes:

1. This switch can be configured as a Single Pole, Single Throw (SPST) by terminating one unused RF switched port with a 50 Ohm load.
2.  $V_C$  can be biased from either pin 1 or 6 and the non-biased pin can be left open.
3.  $V_{C\_N}$  can be biased from either pin 5 or 18 and the non-biased pin can be left open.
4. External components are not required

### Bias Up Procedure

1.  $V_C$  or  $V_{C\_N}$  set to 0 V (see Function Table for RF Path)
2.  $V_{C\_N}$  or  $V_C$  set to -28 V (see Function Table for RF Path)
3. Apply RF signal to RF Input

### Bias Up Down

1. Turn off RF supply
2. Turn  $V_{C\_N}$  or  $V_C$  to 0 V
3. Turn  $V_C$  or  $V_{C\_N}$  to 0 V

### Function Table

RF Path	State	$V_C$	$V_{C\_N}$
RFC to RF1 ON	On-State (Insertion Loss)	-28 V	0 V
	Off-State (Isolation)	0 V	-28 V
RFC to RF2 ON	On-State (Insertion Loss)	0 V	-28 V
	Off-State (Isolation)	-28 V	0 V